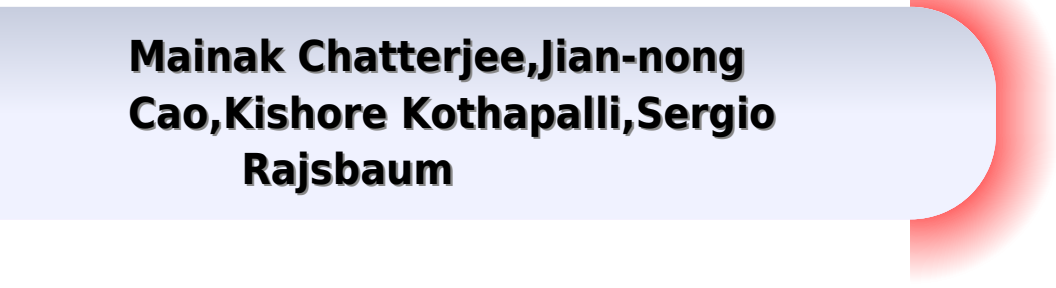


Rachid Guerraoui - Michal Kapalka

Principles of Transactional Memory

Principles Of Transactional Memory Michael Kapalka

**Mainak Chatterjee, Jian-nong
Cao, Kishore Kothapalli, Sergio
Rajsbaum**



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Principles of Transactional Memory Rachid Guerraoui, Michael Kapalka, 2022-06-01 Transactional memory TM is an appealing paradigm for concurrent programming on shared memory architectures With a TM threads of an application communicate and synchronize their actions via in memory transactions Each transaction can perform any number of operations on shared data and then either commit or abort When the transaction commits the effects of all its operations become immediately visible to other transactions when it aborts however those effects are entirely discarded Transactions are atomic programmers get the illusion that every transaction executes all its operations instantaneously at some single and unique point in time Yet a TM runs transactions concurrently to leverage the parallelism offered by modern processors The aim of this book is to provide theoretical foundations for transactional memory This includes defining a model of a TM as well as answering precisely when a TM implementation is correct what kind of properties it can ensure what are the power and limitations of a TM and what inherent trade offs are involved in designing a TM algorithm While the focus of this book is on the fundamental principles its goal is to capture the common intuition behind the semantics of TMs and the properties of existing TM implementations Table of Contents Introduction Shared Memory Systems Transactional Memory A Primer TM Correctness Issues Implementing a TM Further Reading Opacity Proving Opacity An Example Opacity vs Atomicity Further Reading The Liveness of a TM Lock Based TMs Obstruction Free TMs General Liveness of TMs Further Reading Conclusions

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announcements are included in the back matter of the proceedings **Distributed Computing and Networking** Mainak Chatterjee, Jian-nong Cao, Kishore Kothapalli, Sergio Rajsbaum, 2014-01-02 This book constitutes the proceedings of the 15th International Conference on Distributed Computing and Networking ICDCN 2014 held in Coimbatore India in January 2014 The 32 full papers and 8 short papers presented in this volume were carefully reviewed and selected from 110 submissions They are organized in topical sections named mutual exclusion agreement and consensus parallel and multi core computing distributed algorithms transactional memory P2P and distributed networks resource sharing and scheduling cellular and cognitive radio networks and backbone networks *Transactional Memory* Tim Harris, James R. Larus, Ravi Rajwar, 2010 The advent of multicore processors has renewed interest in the idea of incorporating transactions into the programming model used to write parallel programs This approach known as transactional memory offers an alternative and hopefully better way to coordinate concurrent threads The ACI atomicity consistency isolation properties of transactions provide a foundation to ensure that concurrent reads and writes of shared data do not produce inconsistent or incorrect results At a higher level a computation wrapped in a transaction executes atomically either it completes successfully and commits its result in its entirety or it aborts In addition isolation ensures the transaction produces the same result as if no other transactions were executing concurrently Although transactions are not a parallel programming panacea they shift much of the burden of synchronizing and coordinating parallel computations from a programmer to a compiler to a language runtime system or to hardware The challenge for the system implementers is to build an efficient transactional memory infrastructure This book presents an overview of the state of the art in the design and implementation of transactional memory systems as of early spring 2010 **Transactional Memory, Second Edition** Tim Harris, James Larus, Ravi Rajwar, 2022-05-31 The advent of multicore processors has renewed interest in the idea of incorporating transactions into the programming model used to write parallel programs This approach known as transactional memory offers an alternative and hopefully better way to coordinate concurrent threads The ACI atomicity consistency isolation properties of transactions provide a foundation to ensure that concurrent reads and writes of shared data do not produce inconsistent or incorrect results At a higher level a computation wrapped in a transaction executes atomically either it completes successfully and commits its result in its entirety or it aborts In addition isolation ensures the transaction produces the same result as if no other transactions were executing concurrently Although transactions are not a parallel programming panacea they shift much of the burden of synchronizing and coordinating parallel computations from a programmer to a compiler to a language runtime system or to hardware The challenge for the system implementers is to build an efficient transactional memory infrastructure This book presents an overview of the state of the art in the design and implementation of transactional memory systems as of early spring 2010 Table of Contents Introduction Basic Transactions Building on Basic Transactions Software Transactional Memory Hardware Supported Transactional Memory Conclusions Shared-Memory Synchronization Michael L.

Scott,Trevor Brown,2024-01-30 This book offers a comprehensive survey of shared memory synchronization with an emphasis on systems level issues It includes sufficient coverage of architectural details to understand correctness and performance on modern multicore machines and sufficient coverage of higher level issues to understand how synchronization is embedded in modern programming languages The primary intended audience for this book is systems programmers the authors of operating systems library packages language run time systems concurrent data structures and server and utility programs Much of the discussion should also be of interest to application programmers who want to make good use of the synchronization mechanisms available to them and to computer architects who want to understand the ramifications of their design decisions on systems level code *Principles of Transactional Memory* Rachid Guerraoui,Michael

Kapalka,2010-09-24 Transactional memory TM is an appealing paradigm for concurrent programming on shared memory architectures With a TM threads of an application communicate and synchronize their actions via in memory transactions Each transaction can perform any number of operations on shared data and then either commit or abort When the transaction commits the effects of all its operations become immediately visible to other transactions when it aborts however those effects are entirely discarded Transactions are atomic programmers get the illusion that every transaction executes all its operations instantaneously at some single and unique point in time Yet a TM runs transactions concurrently to leverage the parallelism offered by modern processors The aim of this book is to provide theoretical foundations for transactional memory This includes defining a model of a TM as well as answering precisely when a TM implementation is correct what kind of properties it can ensure what are the power and limitations of a TM and what inherent trade offs are involved in designing a TM algorithm While the focus of this book is on the fundamental principles its goal is to capture the common intuition behind the semantics of TMs and the properties of existing TM implementations Table of Contents Introduction Shared Memory Systems Transactional Memory A Primer TM Correctness Issues Implementing a TM Further Reading Opacity Proving Opacity An Example Opacity vs Atomicity Further Reading The Liveness of a TM Lock Based TMs Obstruction Free TMs General Liveness of TMs Further Reading Conclusions *Programming Multicore and Many-core Computing Systems* Sabri Pllana,Fatos Xhafa,2017-01-23

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