

Michael Hübner
Jürgen Becker
Editors

Multiprocessor System-on-Chip

Hardware Design
and Tool Integration

 Springer

Multiprocessor System On Chip Hardware Design And Tool Integration

**Sven Goossens, Karthik
Chandrasekar, Benny Akesson, Kees
Goossens**

Multiprocessor System On Chip Hardware Design And Tool Integration:

Multiprocessor System-on-Chip Michael Hübner, Jürgen Becker, 2010-11-25 The purpose of this book is to evaluate strategies for future system design in multiprocessor system on chip MPSoC architectures Both hardware design and integration of new development tools will be discussed Novel trends in MPSoC design combined with reconfigurable architectures are a main topic of concern The main emphasis is on architectures design flow tool development applications and system design *Retargetable Processor System Integration into Multi-Processor System-on-Chip Platforms* Andreas

Wieferink, Heinrich Meyr, Rainer Leupers, 2008-07-08 This book presents a methodology and the associated tooling for enabling design space exploration as well as a successive refinement flow for the design of optimized MP SoCs with a high degree of automation **Pipelined Multiprocessor System-on-Chip for Multimedia** Haris Javaid, Sri

Parameswaran, 2013-11-26 This book describes analytical models and estimation methods to enhance performance estimation of pipelined multiprocessor systems on chip MPSoCs A framework is introduced for both design time and run time optimizations For design space exploration several algorithms are presented to minimize the area footprint of a pipelined MPSoC under a latency or a throughput constraint A novel adaptive pipelined MPSoC architecture is described where idle processors are transitioned into low power states at run time to reduce energy consumption Multi mode pipelined MPSoCs are introduced where multiple pipelined MPSoCs optimized separately are merged into a single pipelined MPSoC enabling further reduction of the area footprint by sharing the processors and communication buffers Readers will benefit from the authors combined use of analytical models estimation methods and exploration algorithms and will be enabled to explore billions of design points in a few minutes **Multiprocessor Systems on Chip** Torsten Kempf, Gerd Ascheid, Rainer

Leupers, 2011-02-11 This book gives a comprehensive introduction to the design challenges of MPSoC platforms focusing on early design space exploration It defines an iterative methodology to increase the abstraction level so that evaluation of design decisions can be performed earlier in the design process These techniques enable exploration on the system level before undertaking time and cost intensive development **Multicore Systems On-Chip: Practical Software/Hardware**

Design Abderazek Ben Abdallah, 2013-07-20 System on chips designs have evolved from fairly simple uncore single memory designs to complex heterogeneous multicore SoC architectures consisting of a large number of IP blocks on the same silicon To meet high computational demands posed by latest consumer electronic devices most current systems are based on such paradigm which represents a real revolution in many aspects in computing The attraction of multicore processing for power reduction is compelling By splitting a set of tasks among multiple processor cores the operating frequency necessary for each core can be reduced allowing to reduce the voltage on each core Because dynamic power is proportional to the frequency and to the square of the voltage we get a big gain even though we may have more cores running As more and more cores are integrated into these designs to share the ever increasing processing load the main challenges lie in efficient memory

hierarchy scalable system interconnect new programming paradigms and efficient integration methodology for connecting such heterogeneous cores into a single system capable of leveraging their individual flexibility Current design methods tend toward mixed HW SW co designs targeting multicore systems on chip for specific applications To decide on the lowest cost mix of cores designers must iteratively map the device s functionality to a particular HW SW partition and target architectures In addition to connect the heterogeneous cores the architecture requires high performance complex communication architectures and efficient communication protocols such as hierarchical bus point to point connection or Network on Chip Software development also becomes far more complex due to the difficulties in breaking a single processing task into multiple parts that can be processed separately and then reassembled later This reflects the fact that certain processor jobs cannot be easily parallelized to run concurrently on multiple processing cores and that load balancing between processing cores especially heterogeneous cores is very difficult

Embedded Software Design and Programming of Multiprocessor System-on-Chip Katalin Popovici,Frédéric Rousseau,Ahmed A. Jerraya,Marilyn Wolf,2010-03-03 Current multimedia and telecom applications require complex heterogeneous multiprocessor system on chip MPSoC architectures with specific communication infrastructure in order to achieve the required performance Heterogeneous MPSoC includes different types of processing units DSP microcontroller ASIP and different communication schemes fast links non standard memory organization and access Programming an MPSoC requires the generation of efficient software running on MPSoC from a high level environment by using the characteristics of the architecture This task is known to be tedious and error prone because it requires a combination of high level programming environments with low level software design This book gives an overview of concepts related to embedded software design for MPSoC It details a full software design approach allowing systematic high level mapping of software applications on heterogeneous MPSoC This approach is based on gradual refinement of hardware software interfaces and simulation models allowing to validate the software at different abstraction levels This book combines Simulink for high level programming and SystemC for the low level software development This approach is illustrated with multiple examples of application software and MPSoC architectures that can be used for deep understanding of software design for MPSoC

Memory Controllers for Mixed-Time-Criticality Systems Sven Goossens,Karthik Chandrasekar,Benny Akesson,Kees Goossens,2016-04-11 This book discusses the design and performance analysis of SDRAM controllers that cater to both real time and best effort applications i e mixed time criticality memory controllers The authors describe the state of the art and then focus on an architecture template for reconfigurable memory controllers that addresses effectively the quickly evolving set of SDRAM standards in terms of worst case timing and power analysis as well as implementation A prototype implementation of the controller in SystemC and synthesizable VHDL for an FPGA development board are used as a proof of concept of the architecture template

Memory Controllers for Real-Time Embedded Systems Benny Akesson,Kees

Goossens,2011-09-15 Verification of real time requirements in systems on chip becomes more complex as more applications are integrated Predictable and composable systems can manage the increasing complexity using formal verification and simulation This book explains the concepts of predictability and composability and shows how to apply them to the design and analysis of a memory controller which is a key component in any real time system **Handbook of Research on Embedded Systems Design** Bagnato, Alessandra,Indrusiak, Leandro Soares,Quadri, Imran Rafiq,Rossi, Matteo,2014-06-30 As real time and integrated systems become increasingly sophisticated issues related to development life cycles non recurring engineering costs and poor synergy between development teams will arise The Handbook of Research on Embedded Systems Design provides insights from the computer science community on integrated systems research projects taking place in the European region This premier references work takes a look at the diverse range of design principles covered by these projects from specification at high abstraction levels using standards such as UML and related profiles to intermediate design phases This work will be invaluable to designers of embedded software academicians students practitioners professionals and researchers working in the computer science industry **Encyclopedia of Information Science and Technology, Third Edition** Khosrow-Pour, D.B.A., Mehdi,2014-07-31 This 10 volume compilation of authoritative research based articles contributed by thousands of researchers and experts from all over the world emphasized modern issues and the presentation of potential opportunities prospective solutions and future directions in the field of information science and technology Provided by publisher

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