

NEW ALGORITHMS, ARCHITECTURES AND APPLICATIONS FOR RECONFIGURABLE COMPUTING

Edited by
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and
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New Algorithms Architectures And Applications For Reconfigurable Computing

**Nikolaos Voros, Alberto Rosti, Michael
Hübner**



New Algorithms Architectures And Applications For Reconfigurable Computing:

New Algorithms, Architectures and Applications for Reconfigurable Computing Patrick Lysaght, Wolfgang Rosenstiel, 2005-07-01 New Algorithms Architectures and Applications for Reconfigurable Computing consists of a collection of contributions from the authors of some of the best papers from the Field Programmable Logic conference FPL 03 and the Design and Test Europe conference DATE 03 In all seventy nine authors from research teams from all over the world were invited to present their latest research in the extended format permitted by this special volume The result is a valuable book that is a unique record of the state of the art in research into field programmable logic and reconfigurable computing The contributions are organized into twenty four chapters and are grouped into three main categories architectures tools and applications Within these three broad areas the most strongly represented themes are coarse grained architectures dynamically reconfigurable and multi context architectures tools for coarse grained and reconfigurable architectures networking security and encryption applications Field programmable logic and reconfigurable computing are exciting research disciplines that span the traditional boundaries of electronic engineering and computer science When the skills of both research communities are combined to address the challenges of a single research discipline they serve as a catalyst for innovative research The work reported in the chapters of this book captures that spirit of that innovation **New**

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in topical sections on architectures mapping techniques and tools arithmetic and applications **Reconfigurable Computing: Architectures and Applications** Koen Bertels, João M.P. Cardoso, Stamatis Vassiliadis, 2006-08-03 This book constitutes the thoroughly refereed post proceedings of the Second International Workshop on Reconfigurable Computing ARC 2006 held in Delft The Netherlands in March 2006 The 22 revised full papers and 35 revised short papers presented were thoroughly reviewed and selected from 95 submissions The papers are organized in topical sections on applications power image processing organization and architecture networks and communication security and tools **Modern Programmable Interconnect Design** Stefan Nikolić, 2025-03-06 This book thoroughly analyzes the fundamental problem of programmable interconnect design in FPGAs Recent changes in fabrication technology have caused a renewed interest in this problem that was formerly considered largely solved Besides presenting some of the most recent research results directed towards resolving the difficulties of programmable interconnect design in scaled technologies the author also gives a modern introduction to programmable interconnect design in general covering related topics such as broader FPGA architecture CAD and Networks on Chip enabling practitioners from other fields to understand the significance and implications of this important problem **Reconfigurable Computing: Architectures, Tools and Applications** Phaophak Sirisuk, Fearghal Morgan, Tarek El-Ghazawi, Hideharu Amano, 2010-03-10 Reconfigurable computing RC systems have generated considerable interest in the embedded and high performance computing communities over the past two decades with field programmable gate arrays FPGAs as the leading technology at the helm of innovation in this discipline Achieving orders of magnitude performance and power improvements using FPGAs over traditional microprocessors is not uncommon for well suited applications But even with two decades of research and technological advances FPGA design still presents a substantial challenge and often necessitates hardware design expertise to exploit its true potential Although the challenges to address the design productivity issues are steep the promise and the potential of the RC technology in terms of performance power size and versatility continue to attract application design engineers and RC researchers alike The International Symposium on Applied Reconfigurable Computing ARC aims to bring together researchers and practitioners of RC systems with an emphasis on practical applications and design methodologies of this promising technology This year's ARC symposium The sixth ARC symposium was held in Bangkok Thailand during March 17-19 2010 and attracted papers in three primary focus areas RC applications RC architectures and RC design methodologies *Parallel Computing: Software Technology, Algorithms, Architectures & Applications* Gerhard Joubert, Wolfgang Nagel, Frans Peters, Wolfgang Walter, 2004-09-23 Advances in Parallel Computing series presents the theory and use of parallel computer systems including vector pipeline array fifth and future generation computers and neural computers This volume features original research work as well as accounts on practical experience with and techniques for the use of parallel computers *Dynamic System Reconfiguration in Heterogeneous Platforms* Nikolaos Voros, Alberto Rosti, Michael Hübner, 2009-05-28 Dynamic

System Reconfiguration in Heterogeneous Platforms defines the MORPHEUS platform that can join the performance density advantage of reconfigurable technologies and the easy control capabilities of general purpose processors. It consists of a System on Chip made of a scalable system infrastructure hosting heterogeneous reconfigurable accelerators providing dynamic reconfiguration capabilities and data stream management capabilities.

Computer-Hardware Evaluation of Mathematical Functions Amos OMONDI, 2015-10-22. This book provides a thorough up to date understanding of the methods used in computer hardware for the evaluation of mathematical functions: reciprocals, square roots, exponentials, logarithms, trigonometric functions, hyperbolic functions, etc. It discusses how the methods are derived, how they work, and how well they work. The methods are divided into four core themes: CORDIC, normalization, table look up, and polynomial approximations. In each case, the author carefully considers the mathematical derivation and basis of the relevant methods, how effective they are, including mathematical errors analysis, and how they can be implemented in hardware. This book is an excellent resource for any student or researcher seeking a comprehensive yet easily understandable explanation of how computer chips evaluate mathematical functions.

Applied Reconfigurable Computing. Architectures, Tools, and Applications Nikolaos Voros, Michael Huebner, Georgios Keramidas, Diana Goehringer, Christos Antonopoulos, Pedro C. Diniz, 2018-04-25. This book constitutes the proceedings of the 14th International Conference on Applied Reconfigurable Computing (ARC 2018) held in Santorini, Greece, in May 2018. The 29 full papers and 22 short papers presented in this volume were carefully reviewed and selected from 78 submissions. In addition, the volume contains 9 contributions from research projects. The papers were organized in topical sections named: machine learning and neural networks, FPGA based design and CGRA optimizations, applications and surveys, fault tolerance, security and communication architectures, reconfigurable and adaptive architectures, design methods, and fast prototyping, FPGA based design and applications, and special session research projects.

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